



**Sharad Institute of Technology, College of Engineering,  
Yadav - Ichalkaranji.  
(Electronics & Telecommunication Department)**

---



**(A) Personal Data:**

<b>Title (Mr/Ms./Mrs./Dr./Prof.)</b>	Ms
<b>Name</b>	Madhuri Balasaheb Mulik
<b>Programme (UG/PG)</b>	UG
<b>Course / Department</b>	Electronics and Telecommunication
<b>Area of Specialization</b>	Electronics Technology
<b>Exact Designation</b>	Assistant Professor
<b>Address (Residential) (with PIN code)</b>	21/1197, Jawahar Nagar, Mulik Wakhar, Ichalkaranji 416117
<b>E-mail ID</b>	mulik_madhuri2002@yahoo.co.in
<b>Mobile No.</b>	9921660850

**(B) Project Under Taken:**

<b>Sr. No.</b>	<b>Name of the Project</b>	<b>Academic Year</b>
1	“Conveyer Based Presetable Counter With SMS alert”	2015-2016
2	“Accelerometer based gesture controlled Robot”	2015-2016
3	“Vehicle Movement Street Lighting of Highways”	2015-2016
4	“DTMF Based Voting Machine”	2014-2015
5	“Electricity Power Theft Over Electrical Poles”	2014-2015
6	“Networked wireless Meter Reading Based on Zigbee Technology”	2013-2014
7	“Design comparison and implementation of different multipliers using VHDL”	2013-2014
8	“Traffic violation detection”	2012-2013

**(C) Academic Qualifications:**

<b>Exam Passed</b>	<b>Name of the institute</b>
<b>Degree (U.G.)</b>	DKTE'S
<b>P.G.</b>	DOT



**Sharad Institute of Technology, College of Engineering,  
Yadav - Ichalkaranji.  
(Electronics & Telecommunication Department)**

**(D) Experience:**

<b>Teaching Experience (In Years)</b>
8yr 7month

**(E) Conferences, seminars, symposia, training, workshop, STTP, FDP attended**

**(i) Conferences, seminars, symposia, Training, workshop, STTP, FDP attended:**

Sr. No.	Name of the conferences/ seminars/symposia /STTP/FDP/workshop	Name of sponsoring agency	duration		Place
			from	to	
1.	“Pedology to Synergogy”	SITCOE	31th Dec 2015	4 <sup>th</sup> Jan 2016	Yadav
2.	“Advaned Trends in PLC- Automation”	SITCOE	30 <sup>th</sup> Nov 2015	4 <sup>th</sup> Dec 2015	Yadav
3.	“Advanced Techniques in Reasearch Methdology”	SITCOE	15 <sup>th</sup> June 2015	26 <sup>th</sup> June 2015	Yadav
4.	“Embedded System Design Using ARM Cortex LPC 1768 & VLSI system design using Xilinx FPGA”	DKTE’s	12 <sup>th</sup> Feb 2015	14 <sup>th</sup> Feb 2015	Ichalkaranji
5.	“Image Processing & Soft Computing Algorithm Implementations in Interdisciplinary Area”	DKTE’s	17 <sup>th</sup> -19 <sup>th</sup> Jan 2014	24 <sup>th</sup> -25 <sup>th</sup> Jan 2014	Ichalkaranji
6.	“Advanced Trends in Video Engineering”	SITCOE	12 <sup>th</sup> May 2014	16 <sup>th</sup> May 2014	Yadav
7.	“Network simulator2 for Wired & Wireless Network”	SITP	17 <sup>th</sup> October 2013	19 <sup>th</sup> October 2013	YADRAV
8.	Workshop on “Reasearch & methods in educational technology”	IIT Bombay ,Remot center SITCOE,	2 <sup>nd</sup> Feb 2013	9 <sup>th</sup> Feb 2013	Ydrav
9.	Workshop on “Better enhancement of soft skill technology”	SITCOE	17 <sup>th</sup> Dec 2012	27 <sup>th</sup> Dec 2012	Yadav
10.	Workshop on “Aakash Tablet”	SITCOE	10 <sup>th</sup> Nov 2012	11 <sup>th</sup> Nov 2012	Yadav
11.	Workshop on “Teaching methodologies”	SITCOE	15 <sup>th</sup> June, 2012	17 <sup>th</sup> June, 2012	Yadav
12.	Workshop on “EMBEDDED SYSTEM DESIGN”	GPKP	14 <sup>th</sup> May 2012	25 <sup>th</sup> May 2012	Kolhapur
13.	Workshop on “Basic Electronics”	IIT Bombay, Remot Center DKTE’s	28 <sup>th</sup> June 2011	8 <sup>th</sup> July 2011	Ichalkaranji



**Sharad Institute of Technology, College of Engineering,  
Yadav - Ichalkaranji.  
(Electronics & Telecommunication Department)**

14.	Workshop on 'Advanced VLSI Design Techniques'	DKTE's	3 <sup>rd</sup> Jan 2011	8 <sup>th</sup> Jan 2011	Ichalkaranji.

**(F) Paper published in conferences/journals**

**(i) Paper published in conferences:**

Sr. No.	Name of the conference	Title of the paper	Name of sponsoring agency / institute	duration		Place
				from	to	
1	International Conference on "Elwctronics & computing" ICET-2015	Poster presentation "DTMF" Based Voting Machin"	DKTE's	9 <sup>th</sup> April 2015	10 <sup>th</sup> April 2015	Ichalkaranji
2	National conference NCET-2012,	Abstract selected for indexing "Smart Antenna Technology with switched beam & Adaptive array"	DOT Kolhapur			Kolhapur
3	<b>International conference</b> on Emerging Trends in Electronics, (2010)	Paper presented "Design of Viterbi decoder for noisy channel on FPGA"	JJMCOE Jaysinghpur	20 <sup>th</sup> feb 2010	21 <sup>th</sup> feb 2010	Jaysinghpur
4	National Conference on Advances in Recent Trends in ommunication and Networks, (2010)	Paper presented "Design of Viterbi decoder for noisy channel on FPGA"	Anna Dange College,Ashta			Ashta
5	National Conference 'URJITAM-2010' Rajendra Mane College of Engineering and Technology, Ambav,	Paper presented "Design of Viterbi decoder for noisy channel on FPGA"	Rajendra Mane College of Engineering and Technology, Ambav,	13 <sup>th</sup> march 2010	15 <sup>th</sup> march 2010	Ambav, Devrukh-Ratnagiri



**Sharad Institute of Technology, College of Engineering,  
Yadav - Ichalkaranji.  
(Electronics & Telecommunication Department)**

---

**(ii) Paper published in Journals:**

Sr. No.	Name of the journal	Title of the paper	Vol. & Issue	pp	
				from	to
1	International Journal of Engineering Research and Development e-	“Artificial wind Data Generation On moving vehical”	ISSN: 2278-067X, p-ISSN: 2278-800X, www.ijerd.com Volume 6, Issue 10 (April 2013), PP. 90-94	90	94
2	International Journal of Scientific and Research	“Application of wind energy in two wheeler”	Publications 1 ISSN 2250-3153, 2013	2250	3153
4	International Journal of Scientific & Engineering Research,	“Design of Viterbi decoder for noisy channel on FPGA”	ID- 1067063, Volume 2, Issue 6, June 2011		

**(G) Professional Membership:**

Sr. No.	Name of the membership	Membership no.	Type of membership (annual / life)	Name of membership body	duration	
					from	to
1	ISTE	LM83189	Life Membership		2012	

**Pan No. ASDPM8711H  
Adhar Card No.**